

FIG. 4A

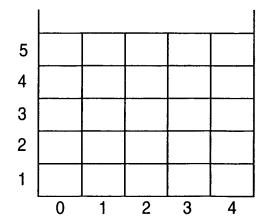


FIG. 4B

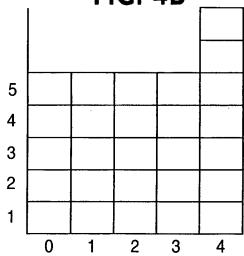
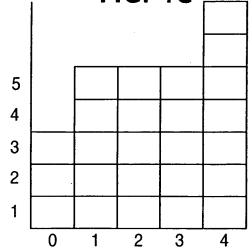
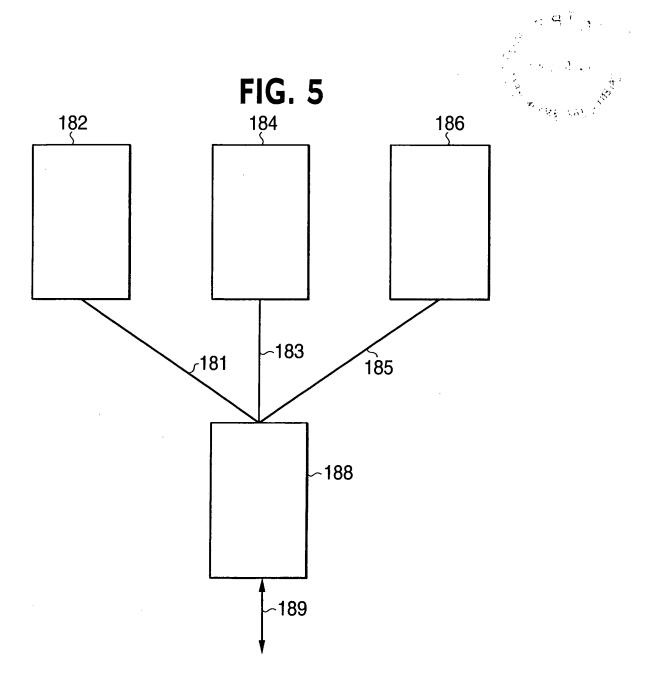


FIG. 4C



B. Commence of the second



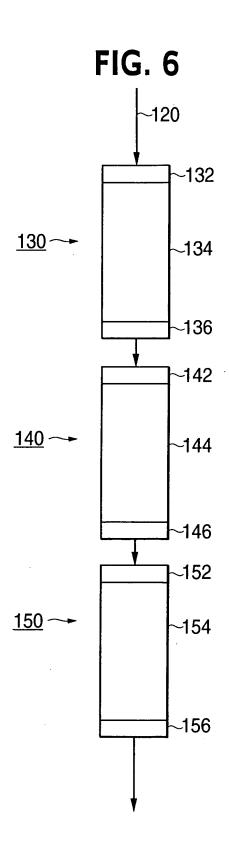




FIG. 7

	172 173						176 }	177
	Q0	Q1	Q2	Q3	Q4	Q5	Q6	Q7
170 171				174				

OUTPUT QUEUES 238 <u>~</u>188 VIRTUAL INPUT QUEUES INPU QUEUES 237 OUTPUT \sim 197 VIRTUAL INPUT QUEUES INPUT OUTPUT QUEUES 236 \sim 196 VIRTUAL INPUT QUEUES INPUT I/F CENTRAL CONTROL AND MAPPING 216 OUTPUT QUEUES 235 INPUT //F/ \sim 195 VIRTUAL INPUT QUEUES 215 OUTPUT QUEUES 234 \sim 194 VIRTUAL INPUT QUEUES INPUT \sim 193 OUTPUT QUEUES 233 INPUT VIRTUAL INPUT 213 \sim 192 OUTPUT QUEUES 232 VIRTUAL INPUT QUEUES INPUT 212 VIRTUAL INPUT QUEUES ~191 INPUT I/F OUTPUT QUEUES 231

007-

230

OUTPUT

OUTPUT I/F

OUTPUT I/F

OUTPUT I/F

OUTPUT I/F

OUTPUT I/F

OUTPUT I/F

OUTPUT I/F

257~

7246

7245

7244

-243

_242

7241

251~

,220

FIG. 8

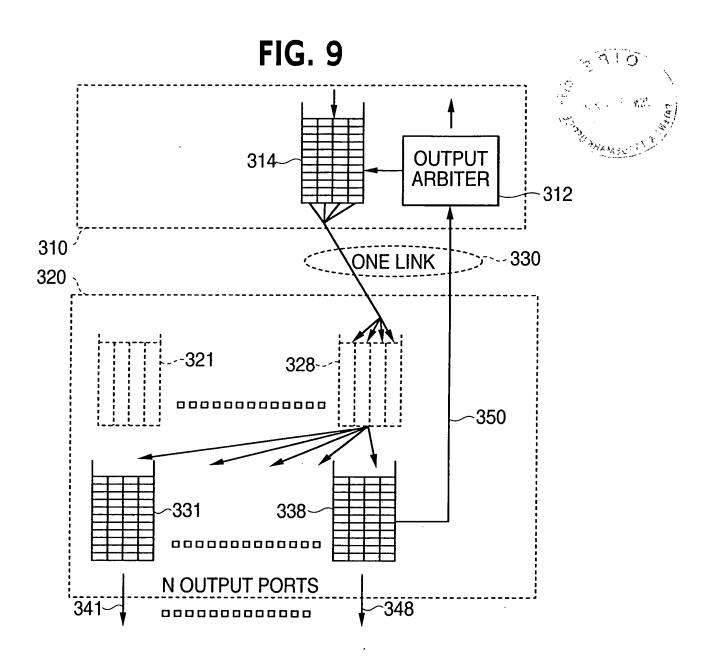


FIG. 10

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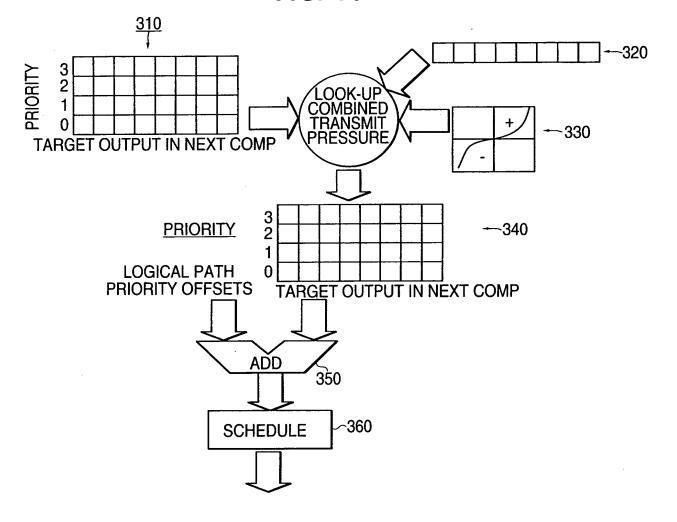
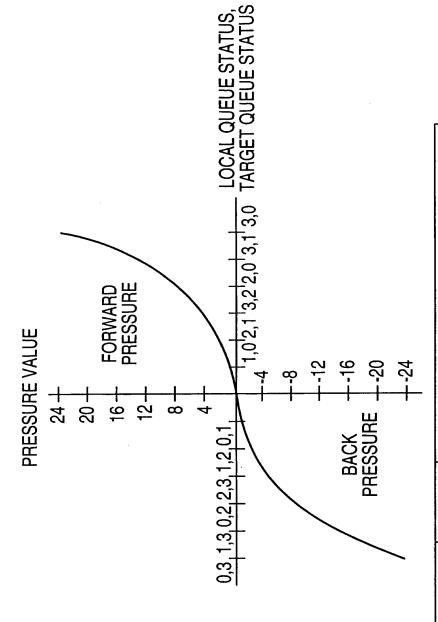


FIG. 11



COMP	3	-24	-14	-4	0
STATUS OF TARGET QUEUE IN NEXT COMP	2	8-	-2	0	4
AHGE! QU	1	 -	0	2	14
AIUSOF	0	0	1	8	24
5		0	-	2	3
		STATUS	OF	LOCAL	QUEUE

